Product Life-cycle Support Notice
Last Time Buy for Mark* Vle UCSB Controllers
Order Deadline: February 2016

Per previous communications, Intel® has announced a Last Time Buy (LTB) on the primary CPU component in the Mark Vle UCSB controller product line. At that time we re-prioritized our engineers to work on the next generation of Mark Vle controller under the Industrial Internet Control System program. This controller family will be called the UCSC and will be enabled as a hypervised solution allowing the connection to the internet as a field agent edge node and will be ready to run Predix™ applications.

As a result, GE’s Automation & Controls team needs to finalize the coordinated business plan for GE. Thus we are communicating the LTB of the impacted UCSB controller and the following part numbers:

- IS420UCSBH1A
- IS420UCSBS1A
- IS420UCSBH3A
- IS420UCSBH4A

There are three distinct subcomponent CPU chips impacted within this family and thus we need to manage this plan per UCSB part number.

The following table lists the milestones for the LTB plan. Each client business needs to evaluate their needs under the plan.

<table>
<thead>
<tr>
<th>Date</th>
<th>Step</th>
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<tbody>
<tr>
<td>29–Jan–2016</td>
<td>Client business to provide quantities of UCSB part numbers required during the transition (Jan 2016 to Dec 2017)</td>
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<tr>
<td>Feb 2016</td>
<td>Automation &amp; Controls will issue a LTB PO to Intel for the combined requirements of GE client businesses</td>
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<tr>
<td>Feb 2016</td>
<td>Client businesses to issue a LTB PO to Automation &amp; Controls for the required UCSB controllers per part numbers</td>
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<tr>
<td>Jun 2018</td>
<td>Last opportunity to submit a job-specific PO containing the UCSB part number</td>
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At the end of each quarter in this transition period the LTB PO quantity will be adjusted downward based on the shipped quantity in the quarter. This will allow a joint tracking of the UCSB parts supplied and how many remain. In June 2018, the unused volume will be reconciled by adjusting the PO to match the unused quantity. We will also adjust the price of the PO so that the client business will only be charged the rate of the unconsumed CPU stock rather than the full price of the controller. The price will be $100/unused CPU.
We recognize the conversion will take time; therefore, we have provided 24 months from this announcement and 23 months from the required LTB PO to complete the transition.

Assumptions that can be used for your conversion planning are as follows:

1. UCSC available for all applications November 2016 except:
   a. Mark VIe Safety Controller
   b. Redundant/Multi-bridge EX2100e configurations
2. Safety controller will be available December 2017
3. UCSC-based EX2100e redundant/multi-bridge configurations to be supported June 2017
4. Purchased Prototypes will be available to ship September 2016

Please contact one of the following product line leaders for clarification or questions:
   Rob Turner – Mark VIe Product Line Manager
   Bob Ridgway – Renewables Vertical Product Line Leader
   Jim Blanchard – GE Power Vertical Product Line Leader
   Mark Hammer – O&G and M&C Vertical Product Line Leader

Sincerely,

Rob Turner
Mark VIe Product Line Manager
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